



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re the Application of: OHASHI, Hitoshi et al.

Group Art Unit: 1792

Serial No.: 10/047,992

Examiner: TALBOT, Brian K.

Filed: January 17, 2002

P.T.O. Confirmation No.: 5363

For: METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT FIXING THE
SEMICONDUCTOR DEVICE USING SUBSTRATE JIG

NOTICE OF APPEAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: June 9, 2008

Sir:
Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated
January 8, 2008 of the Primary Examiner.

The item(s) checked below are appropriate:

1. ☒ A petition for a two-month extension of time to respond to the Final Rejection was filed herewith.
2. ☐ A timely response to the Final Rejection has been filed, as provided in 37 C.F.R. 1.113.
3. ☒ Fee - \$510.00

☐ Enclosed
☐ Not required (fee paid in prior appeal)
☒ Charge to Deposit Account No. 01-2340

In the event this Notice of Appeal is not considered to be timely filed, Applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2340, along with any other additional fees which may be required with respect to this Notice.

Respectfully submitted,
KRATZ, QUINTOS & HANSON, LLP

Darren Crew

Darren Crew
Attorney for Applicants
Reg. No. 37,806

06/10/2008 SZEWDIE1 00000024 012340 10047992
01 FC:1401 510.00 DA

DC/llf

Atty. Docket No. 020052
Suite 400
1420 K Street, N.W.
Washington, D.C. 20005
(202) 659-2930



23850

PATENT & TRADEMARK OFFICE